

**S/N 10/681,481**

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Paul A. Farrar

Examiner: Nicole Blan

Serial No.: 10/681,481

Group Art Unit: 1792

Filed: October 8, 2003

Docket No.: 1303.112US1

Confirmation No.: 7468

Title: METHOD OF CLEANING SEMICONDUCTOR SURFACES

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**RESPONSE UNDER 37 C.F.R. § 1.111**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

The following responds to the Office Action mailed September 18, 2008.